



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Reply, Application of:

Robert S. Chau et al.

Examiner: Pham, Hoai V.

U.S. Serial No: 10/607,632

Art Unit: 2814

Filed: June 27, 2003

For: NONPLANAR DEVICE WITH STRESS
INCORPORATION LAYER AND METHOD
OF FABRICATION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

on April 28, 2004
Date of Deposit
TERESA MATTOX
Name of Person Mailing Correspondence
TERESA MATTOX 4/28/04
Signature Date

Commissioner of Patents and Trademarks
P.O. Box 1405
Alexandria, VA 22313-1450

RESPONSE TO ELECTION REQUIREMENT

Sir:

In response to the Election Requirement mailed April 21, 2004, applicant(s) hereby elects to prosecute Group I claims 1-16 drawn to a semiconductor device. As such, applicant(s) elects to withdraw Group II claims 17-29 drawn to a method of making a semiconductor device.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 4/28, 2004

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